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<p>(21) International Application Number: PCT/GB94/00467 (22) International Filing Date: 10 March 1994 (10.03.94) (30) Priority Data: 055,688 30 April 1993 (30.04.93) US (71) Applicant: PRIME VIEW HK LIMITED [GB/GB]; Suite 1507, Chinachem Golden Plaza, 77 Mody Road, Tsimshatsui East, Kowloon (HK). (72) Inventors: LEE, Sywe, Neng; 111 5 Fl. Wen De Road, Nei-Hu, Taipei (TW). HY, Dyi-Chung; 4F, 153-1, Shung-Shi Village, Pausang County, Shinchu Shein (TW). (74) Agent: BRUNNER, Michael, John; Gill Jennings & Every, Broadgate House, 7 Eldon Street, London EC2M 7LH (GB).</p>		<p>(81) Designated States: AT, AU, BB, BG, BR, BY, CA, CH, CN, CZ, DE, DK, ES, FI, GB, HU, JP, KP, KR, KZ, LK, LU, LV, MG, MN, MW, NL, NO, NZ, PL, PT, RO, RU, SD, SE, SI, SK, TT, UA, UZ, VN, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, ML, MR, NE, SN, TD, TG). Published With international search report.</p>
<p>(54) Title: APPARATUS FOR RECOVERY OF THRESHOLD VOLTAGE SHIFT IN AMORPHOUS SILICON THIN-FILM TRANSISTOR DEVICE</p> <div data-bbox="548 1161 1198 1612"> </div> <p>(57) Abstract</p> <p>An apparatus is provided for recovery of a threshold voltage, V_{th}, of thin-film amorphous silicon transistors (12) deposited on a substrate (10) and a gate voltage is applied to each of the transistors (12) during use, causing V_{th} to shift with time. The apparatus detect when the LCD display (10) is not being used, generates a voltage V_g', of opposite polarity with respect to V_g when the LCD display is not being used, and applies V_g' to the gates of the transistors (12) of the LCD display (10) for causing V_{th} to shift in a direction opposite to that caused by V_g, thereby maintaining an effective driving voltage for the transistor (12) of the LCD display (10).</p>		

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